



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Seung Ki Joo et al.  
Serial No.: 10/038,990  
Filing Date: January 4, 2002  
Examiner: Thao X. Le  
Group Art Unit: 2814  
Title: *Method of Fabricating a Semiconductor Device  
Including a Crystalline Active Silicon Layer*

RECEIVED  
JUL 30 2003  
TECHNOLOGY CENTER 2800

**MAIL STOP AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**"EXPRESS MAIL"**

Express Mailing Label Number  
EV 324625485 US  
Date of Deposit July 23, 2003  
I hereby certify that this paper or fee is  
being deposited with the United States  
Postal Service "Express Mail Post  
Office to Addressee" service under 37  
CFR 1.10 on the date indicated above  
and is addressed to Commissioner for  
Patents, P.O. Box 1450, Alexandria,  
VA 22313-1450.

*Willie Jiles*  
Willie Jiles

**RESPONSE PURSUANT TO 37 C.F.R. § 1.116**

Applicants hereby respond to the Office Action dated April 23, 2003.